



Powering the Future

HPC • AI • QUANTUM



**SUPER
COMPUTING
INDIA 2025**

DECEMBER 09 – 13, 2025

Venue: Manipal Institute of Technology (MIT), Bengaluru - KA - India

ORGANISED BY

Centre for Development of Advanced Computing (C-DAC), Bengaluru



<https://sci25.supercomputingindia.org>

At the forefront of India's technological revolution stands premier R&D organisation **Centre for Development of Advanced Computing (C-DAC)**, established in 1988 under the aegis of the Ministry of Electronics and Information Technology (MeitY). C-DAC pioneered the PARAM series of supercomputers placing India firmly on the global map as a leader in High Performance Computing (HPC).

Today, India is undergoing a remarkable digital and technological transformation, driven by flagship national initiatives including **National Supercomputing Mission**, India AI, India Semiconductor Mission, and National Quantum Mission, spearheaded by MeitY and Department of Science and Technology (DST), the twin pillars of India's science and technology governance.

The National Supercomputing Mission (NSM) has established a robust, distributed network of supercomputing infrastructure across 28 premier institutions nationwide laying the foundation for cutting-edge research, innovation, and capacity building in HPC.

Together, the strategic initiatives embody a bold national vision to position India as a global technology powerhouse. C-DAC remains at the core of this effort, driving advancements in HPC, Artificial Intelligence, and Quantum Computing, while fostering technological self-reliance and building collaborative platforms that bridge academia, industry, and government.

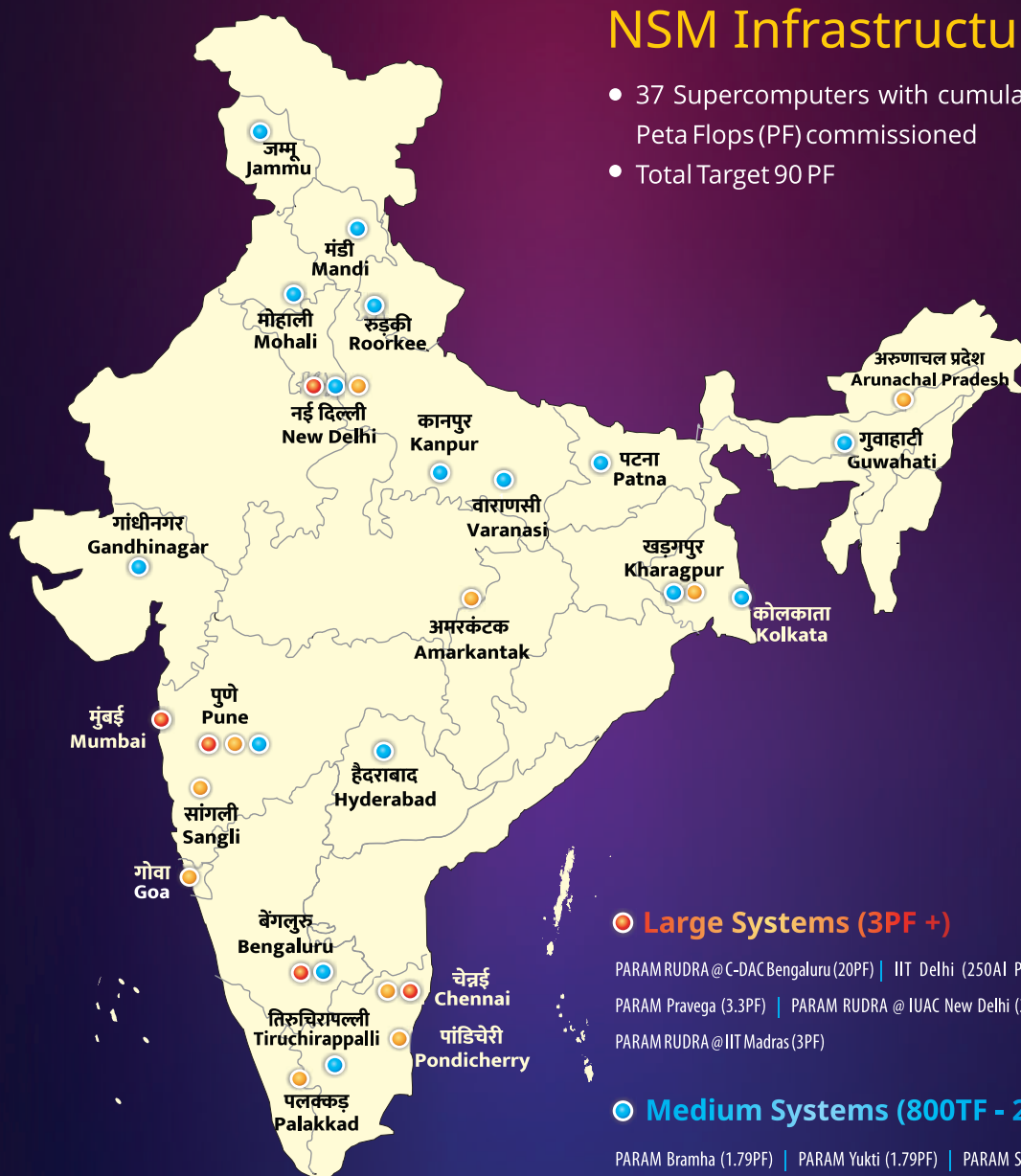
The Supercomputing Conference is a global event for High Performance Computing, networking, storage, and data analysis. The conference unites researchers, industry experts, and government leaders to showcase groundbreaking innovations and share cutting-edge research. Serving as a vital hub for collaboration and knowledge exchange, the event drives the advancement of computational science and engineering in the areas of HPC, AI, Quantum.

Supercomputing India (SCI2025) is the inaugural edition of Supercomputing Conference series in India which will serve as the platform to showcase and explore the latest breakthroughs in Supercomputing, AI, and Quantum technologies, enabling global collaboration at the intersection of science, technology, and innovation.

National Supercomputing Mission

NSM Infrastructure

- 37 Supercomputers with cumulative capacity of 39 Peta Flops (PF) commissioned
- Total Target 90 PF



Large Systems (3PF +)

PARAM RUDRA @ C-DAC Bengaluru (20PF) | IIT Delhi (250AI PF / 6PF) | PARAM Siddhi AI (5.26PF)
PARAM Pravega (3.3PF) | PARAM RUDRA @ IUAC New Delhi (3PF) | PARAM RUDRA @ IIT Bombay (3PF)
PARAM RUDRA @ IIT Madras (3PF)

Medium Systems (800TF - 2PF)

PARAM Bramha (1.79PF) | PARAM Yukti (1.79PF) | PARAM Shakti (1.66PF) | PARAM Sanganak (1.66PF)
PARAM Ganga (1.66PF) | NIC Delhi (50AI PF / 1.3PF) | PARAM RUDRA @ GMRT/NCRA Pune (1PF)
PARAM Seva (838TF) | PARAM Smriti (838TF) | PARAM Utkarsh (838TF) | PARAM Porul (838TF)
PARAM Himalaya (838TF) | PARAM Shivay (838TF) | PARAM Kamrupa (838TF)
PARAM RUDRA @ IIT Patna (838TF) | PARAM Ananta (838TF) | PARAM RUDRA @ SNBNCBS Kolkata (838TF)
PARAM RUDRA @ IIT Jammu (838TF)

Small Systems (less than 500TF)

PARAM Spoorthi (100TF) | PARAM VIDYA @ Walchand CoE Sangli (52TF) | PARAM VIDYA @ DTU New Delhi (52TF)
PARAM VIDYA @ IGNTU Amarkantak (52TF) | PARAM VIDYA @ NIT Arunachal Pradesh (52TF)
PARAM VIDYA @ IIT Goa (52TF) | PARAM VIDYA @ Pondicherry University (52TF)
PARAM VIDYA @ IIT Madras (52TF) | PARAM VIDYA @ C-DAC Pune (52TF) | PARAM VIDYA @ IIT Palakkad (52TF)
PARAM VIDYA @ IIT Kharagpur (52TF)

High Performance
RUDRA SERVER



High Performance
Network
TRINETRA

Map not to Scale



Event Spectrum

- Plenary
- Keynotes and Expert Talks/Panels
- Tutorials & Workshops
- HPC Across the Globe
- HPC User Meet
- Param Shavak User Meet
- Cybersecurity in HPC
- Exascale Architectures
- Ethical and Responsible AI
- AI for a trusted and resilient future
- Standardization
- Quantum on the Horizon
- Chip Design Conclave
- Birds-of-a-Feather
- Women in Tech (HPC, AI, Quantum)
- Doctoral Symposium

Pavilions

- High Performance Computing
- Quantum Computing
- Artificial Intelligence
- Government / Public Sector
- Chip Design
- Research & Academia
- Startups
- MSMEs

Event Highlights

 100+ SESSIONS	 200+ SPEAKERS	 5000+ ATTENDEES
 2000+ DELEGATES	 200+ EXHIBITORS	 15+ COUNTRIES
 50+ POSTERS	 500+ RESEARCHERS	 500+ ACADEMICIANS



Conference Focus

SCI2025 will bring together a distinguished global community of experts, researchers, industry leaders, innovators and academicians to chart the future of High Performance Computing (HPC) and its convergence with AI, and Quantum technologies. The conference will feature comprehensive tracks covering next-generation HPC architectures, AI-driven scientific discovery, Quantum innovation, semiconductor ecosystems, and real-world applications across diverse sectors.

Complementing the technical sessions, a cutting-edge technology exhibition will showcase the latest advancements in supercomputing systems, AI accelerators, Quantum-classical hybrid platforms, chip design, HPC software, high-speed interconnects, and security solutions. The exhibition floor will feature participation from global supercomputing centers, leading technology companies, premier R&D and academic institutions, and emerging startups, offering a unique opportunity to explore innovation, foster collaboration, and engage with the forefront of digital transformation.

Who Will You Meet at SCI 2025

At SCI2025, you will connect with a diverse community driving the future of supercomputing, AI, Quantum, and Semiconductors:

- C-suite leaders, CTOs, and decision-makers from global technology corporations
- Policy makers, regulators, and senior government officials from MeitY, DST, and state bodies
- Global experts, researchers, and academicians in HPC, AI, Quantum, and Semiconductors
- Startups, entrepreneurs, and innovators showcasing deep-tech breakthroughs
- Investors, venture capitalists, and funding agencies exploring disruptive technologies
- Industry professionals and domain experts from sectors like automotive, aerospace, healthcare, pharma, advanced manufacturing, and robotics
- Technology buyers, procurement heads, and R&D institutions seeking cutting-edge solutions
- International delegates and trade councils from Asia-Pacific, Europe, and the US
- Students, young researchers, and innovators participating in competitions and workshops
- Media, analysts, and thought leaders covering technology, science, and innovation

Programs at a Glance



Workshops & Tutorials

Hands-on learning with domain experts. Gain practical skills, explore tools, and apply knowledge to real-world challenges.



Technical Sessions

Cutting-edge research, real-world applications. Dive deep into HPC, AI, Quantum, and beyond with experts from around the globe.



Expert Panels

Insights from the frontlines of innovation. Hear industry leaders discuss trends, challenges, and opportunities driving the tech ecosystem.



Exhibitions & Demos

Experience live technical showcases. Explore innovations in supercomputing, AI, Semiconductors, and Quantum, all under one roof.



Women in Technology

Empowering Women in HPC, AI and Quantum through expert insights, dynamic sessions, and inspiring innovation showcases.



Collaboration Forum

Connect. Collaborate. Co-create. A dedicated space for networking across academia, industry, and government.



Birds-of-a-Feather Sessions

Informal, focused discussions on emerging tech. Connect, share ideas, and collaborate with peers in an open, interactive setting.

Special Programs

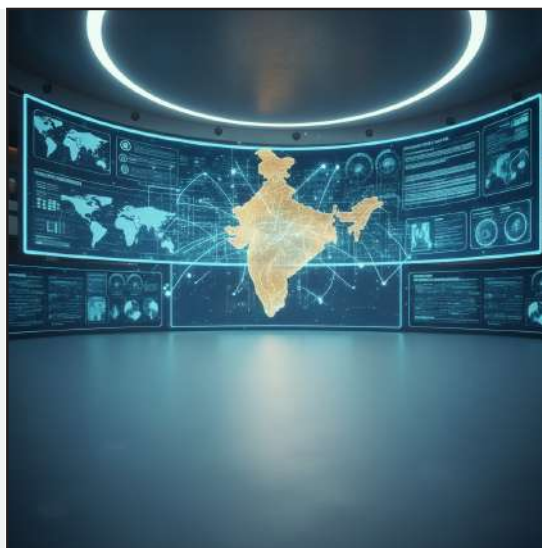


HPC Across the Globe

Track in conference celebrates the international spirit of HPC, bringing together thought leaders, practitioners, and innovators from across the globe. Through open dialogue, knowledge exchange, and shared goals, we continue to advance the frontiers of technology and innovation. Together, we are building a more connected and computationally empowered world.

HPC User Meet

The HPC User Meet brings together pioneers and stakeholders from across the supercomputing ecosystem to accelerate India's journey in high-performance computing. This event highlights key milestones of the National Supercomputing Mission and showcases innovative projects driving scientific and technological breakthroughs. Be part of the HPC User Meet and contribute to shaping the future of supercomputing in India.



AI for a trusted and resilient future

Artificial Intelligence is transforming our world, but building trust and resilience is key to its success. This session focuses on how AI can be developed and used responsibly to ensure fairness, security, and reliability. Experts will discuss ways to create AI systems that people can trust, while making sure these technologies remain strong and adaptable in the face of challenges. Join us to explore how AI can help build a safer, more dependable future for all.

Special Programs



Quantum on the Horizon

The track explores the latest breakthroughs, challenges, and opportunities in quantum research and development. Experts from around the world will share insights on how quantum technologies are shaping the future and what lies ahead on this exciting horizon. Join us to discover how quantum is transforming tomorrow's technology today.

Women in Tech (HPC, AI, Quantum)

Women are driving transformative change in the fields of High-Performance Computing (HPC), Artificial Intelligence (AI), and Quantum Technologies. The track celebrates their achievements, highlights their leadership, and explores the unique challenges they face in these cutting-edge domains.

Join us in recognising the power of diverse perspectives to shape the future of HPC, AI, and Quantum.



PARAM SHAVAK

User Meet 2025

11th December @ SCI 2025
Venue: Manipal Institute of Technology (MIT),
Bengaluru - KA - India

Param Shavak User Meet @ SCI 2025

The PARAM Shavak User Meet, organized by C-DAC under the National Supercomputing Mission (NSM) at Supercomputing India, is a collaborative platform for faculty, researchers, and students to engage with India's indigenous HPC ecosystem. It highlights advancements in high-performance computing, AI, and emerging technologies, empowering academia to adopt indigenous solutions and contribute to India's vision of Viksit Bharat and Atmanirbhar Bharat through innovation and technological excellence.

Curtain Raiser of Supercomputing India 2025

The Lalit Hotel, 14th October 2025, Bengaluru



At the launch of Supercomputing India 2025, the Ministry of Electronics and Information Technology (MeitY) announced the development of a new supercomputer by the Centre for Development of Advanced Computing (C-DAC) under the National Supercomputing Mission. The system, comprising over 3,000 servers, is undergoing testing at C-DAC Bengaluru and is expected to be fully operational by December 2025. Once deployed, it will significantly boost India's computational capacity across critical domains such as weather forecasting, climate modeling, space exploration, genomics, drug discovery, and national security. The new infrastructure will also advance research in urban sustainability, flood prediction, and telecommunications. To further strengthen collaboration in high-performance computing (HPC), artificial intelligence (AI), and quantum technologies, C-DAC will host Supercomputing India 2025: Powering the Future – HPC, AI, Quantum from December 9–13, 2025, at the Manipal Institute of Technology, Bengaluru.



At the Curtain Raiser of SCI2025, Shri S. Krishnan, IAS, Secretary, Ministry of Electronics & Information Technology (MeitY), announced the upcoming launch of a 30-petaflop supercomputer in Bengaluru, reinforcing India's drive toward technological self-reliance. Under the National Supercomputing Mission, 37 supercomputers with a combined capacity of 39 petaflops have already empowered over 10,000 researchers and produced 1,250+ research papers. The initiative highlights India's growing strength in HPC, semiconductor design, and quantum technologies, led by C-DAC's indigenous developments and supported by MeitY's Design Linked Incentive Scheme under the India Semiconductor Mission.

At the Curtain Raiser of SCI2025, Shri Magesh Ethirajan, Director General, C-DAC, highlighted the organisation's leadership in advancing AI, Quantum, and Supercomputing technologies. He outlined the achievements of the National Supercomputing Mission, which has trained over 10,000 researchers, and noted milestones like the Rudra and PARAM series—India's indigenously developed systems. He invited participants to join SCI2025 to explore the nation's growing high-performance computing ecosystem.



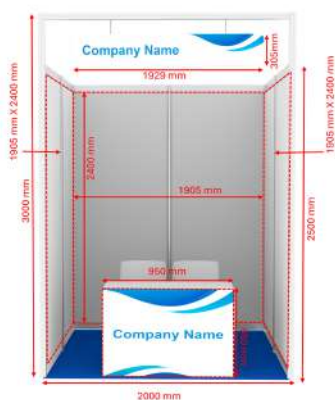
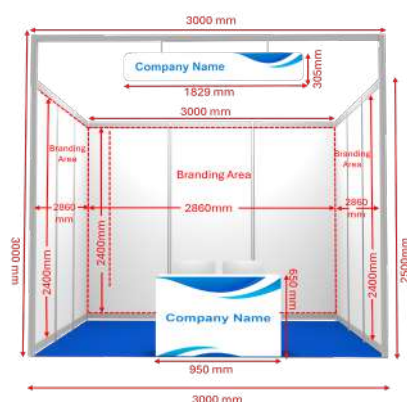
About Tech Expo

Step into the SCI2025 Technology Exhibition – a global showcase of innovation where 200+ exhibitors from India and across the world come together under one roof.

Explore the future of supercomputing, AI, quantum technologies, chip design, and next-gen digital infrastructure. From leading technology corporations and research institutions to startups and deep-tech innovators, the exhibition offers a unique opportunity to witness breakthroughs that are reshaping industries.

Whether you are looking to discover cutting-edge solutions, build global collaborations, or experience live demos of tomorrow's technologies, the SCI2025 Exhibition is the place where ideas transform into action and connections power the next wave of digital transformation.

Exhibition Space Tariff



Space Type	Rate (INR/SQ MTS)	Rate (USD/SQ MTS)
Shell Space	₹ 13,000	\$ 200
Raw Space	₹ 12,000	\$ 175

Terms & Conditions: 18% GST Applicable | Raw Space minimum 36sqm

ENTITLEMENTS: Each 9 SQM Booth

- Fully furnished booth with wall-to-wall carpet, 1 table, 2 chairs, 3 spotlights, and one 5 amp electrical connection. One complimentary delegate registration.
- Two exhibitor badges to manage the booth.
- Listing of organisation information in the event directory.

Startup Innovation Zone:

Type	Price	Special Offer
Startup Booth	₹ 52000	₹ 30000

Terms & Conditions: 18% GST Applicable | The above price applies only to startups that are less than 7 years old.

ENTITLEMENTS: Each Startup Booth

- Booth with carpet, 1 table, 2 chairs, 2 spotlights, and one 5 amp electrical connection. One complimentary delegate registration.
- Two exhibitor badges to manage the booth.
- Listing of organisation information in the event directory.

For Exhibition Queries : Sathyakanth Manukar

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Visitor Profile

- Industry professionals from automotive, aerospace, semiconductors, healthcare, pharma, manufacturing, and robotics.
- International delegates from Asia-Pacific, Europe, and the US.
- Government policymakers from central and state agencies.
- State delegations showcasing regional tech initiatives.
- Start-ups, investors, and deep-tech entrepreneurs exploring innovation and collaboration.
- Researchers, academicians, and students in HPC, AI, and Quantum.

Global Collaboration

With participation from leading countries across Asia-Pacific, Europe, and North America, SCI2025 aims to strengthen cross-border R&D efforts, promote open standards and interoperability, and create new opportunities for collaborative projects in science, industry, and governance.

By connecting global ecosystems, SCI2025 reinforces India's commitment to international cooperation in shaping a secure, inclusive, and innovation-driven digital future.

Sponsorship Opportunities

Be a Sponsor @ SCI2025 and engage with the heart of India's advanced computing ecosystem to:

- **Network** with policymakers, industry pioneers, and academic leaders.
- **Showcase** Leadership via keynote/industry spotlight sessions.
- **Access** High-Impact Visibility across sessions, exhibition, and networking events.
- **Explore** collaboration with National & International Mission

Sponsorship Catagories

- | | | |
|---------------------------------------|----------------------|---------------------------------------|
| • Event Sponsor (Exclusive) | • Silver Sponsor | • Conference Track (HPC, AI, Quantum) |
| • Platinum Sponsor | • State Sponsor | • Workshop and Tutorial Sponsor |
| • Diamond Sponsor | • Conference Co-Host | • Gala Dinner Sponsor |
| • Industry Sponsor (HPC, AI, Quantum) | • Session Sponsor | • Exhibitor Networking Dinner Sponsor |
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Click QR Code for more details and entitlements

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VENUE SPONSOR



MANIPAL INSTITUTE OF TECHNOLOGY
BENGALURU
(A constituent unit of MAHE, Manipal)



SUPER
COMPUTING
INDIA 2025

DECEMBER 09 – 13, 2025

Venue: Manipal Institute of Technology (MIT)

Govindapura, Yelahanka, Bengaluru - 560063, Karnataka, India

SCI Secretariat



Centre for Development of Advanced Computing (C-DAC)

No.1, Old Madras Road, Baiyappanahalli, Bengaluru-560038. Karnataka, INDIA

General Queries: sci@cdac.in

Tech Expo Queries: sci-expo@cdac.in

<https://sci25.supercomputingindia.org>

For more details,
click here



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